

U.S. Department of Commerce, Patent and Trademark Office

Atty Docket No.

Serial No.

M-799-1D US

Unknown

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Applicant

(Use several sheets if necessary)

Constantin Bulucea et al.

Filing Date

Group 2500

Herewithin

Unknown

U.S. Patent Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
AA	4,767,722	08/30/88	Blanchard	357	23.4	
AB	4,595,302	06/03/86	Lidow et al.	357	23.4	
AC	4,824,793	04/25/89	Richardson et al.	437	38	11/12/87
AD	4,663,644	05/05/87	Shimizu	357	23.4	
AE	4,794,561	12/27/88	Hsu	357	23.4	07/02/87
AF	4,631,564	12/23/86	Neilson et al.	357	23.4	
AG	4,680,853	07/21/87	Lidow et al.			
AH	4,893,160	01/09/90	Blanchard	357	23.4	11/13/87
AI	4,914,058	04/03/90	Blanchard	437	41	
AJ	5,019,522	05/15/91	Meyer et al.	437	40	
AK	4,953,535	01/08/91	Blanchard	437	40	

Foreign Patent Documents

							Translation	
	Document	Date	Country	Class	Subclass		Yes	No
AL	0037965	02/15/87	Japan	437	40			
AM	62-12167	01/21/87	Japan	437	41			
AN	3028561	02/15/82	Germany	437	38			
AO								
AP								

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AR	Bulucea, C. , "Avalanche Injection into the Oxide in Silicon Gate-Controlled Devices I. Theory," Solid-State Electronics, 1975, Vol. 18, pp. 363-374.
AS	Bulucea, C., "Avalanche Injection into the Oxide in Silicon Gate-Controlled Devices II. Experimental Results," Solid-State Electronics, 1975, Vol. 18, pp. 381-391.
AT	Marcus and Sheng, "The Oxidation of Shaped Silicon Surfaces," Journal Electrochemical Society: Solid-State Science and Technology, Vol. 129 (1982), pp. 1278-1282.

Examiner

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

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*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
AA	5,016,068	05/14/91	Moai	357	23.4	
AB	4,398,339	8/16/83	Blanchard et al.	437	66	
AC	4,505,543	02/28/89	Parrillo et al	437	38	
AD	4,764,481	08/16/88	Alvi et al.	437	41	
AE	4,506,435	03/26/85	Pliskin et al.	437	67	
AF	5,160,491	11/03/92	Moai	437	203	
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Foreign Patent Documents

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AR	D. Ueda, H. Takagi and G. Kano, "A New Vertical Power MOSFET Structure with Extremely Reduced On-resistance" IEEE Trans. Electron Devices, vol. ED-32 (1985) pp. 2-6.
AS	H.R. Chang, et al., "Self-aligned UMOSFET's with a Specific On-resistance of 1 m Ω - cm ² " IEEE Trans. Electron Devices, vol. ED-34 (1987) pp. 2329-2334.
AT	Wilson and Marcus, "Oxidation of Curved Silicon Surfaces", Journal Electrochemical Society: SOLID-STATE SCIENCE AND TECHNOLOGY, vol. 134 (1987) pp. 481-490.

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							Yes	No
	AL							
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AR K. Yamabe and K. Imai "Nonplanar Oxidation and Reduction of Oxide Leakage Currents at Silicon Corners by Rounding-off Oxidation" IEEE Trans. Electron Devices, vol. ED-34 (1987) pp. 1681-1687.

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08/85/6th
08/086,976

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Constantin Bulucea, et al.

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07/02/93

2508

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	AB	4,808,543	02/28/89	Parrillo			
	AC	4,593,302	06/03/86	Lidow			
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	AJ						
	AK						

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	AS	
	AT	
Examiner	Date Considered 21 Dec 1994	

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